

Titles of Most Frequently Occurring Classifications of Patents Returned
From A Search of 10761060 on September 08, 2005

- 5 712/245 (3 OR, 2 XR)
Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: PROCESSING ARCHITECTURES AND INSTRUCTION
PROCESSING
712/220 PROCESSING CONTROL
712/245 .Processing sequence control (i.e.,
microsequencing)
- 4 345/419 (2 OR, 2 XR)
Class 345 : COMPUTER GRAPHICS PROCESSING, OPERATOR
INTERFACE PROCESSING, AND SELECTIVE VISUAL DISPLAY
SYSTEMS
345/418 COMPUTER GRAPHICS PROCESSING
345/419 .Three-dimension
- 3 708/520 (2 OR, 1 XR)
Class 708 : ELECTRICAL COMPUTERS: ARITHMETIC PROCESSING
AND CALCULATING
708/100 ELECTRICAL DIGITAL CALCULATING COMPUTER
708/200 .Particular function performed
708/490 ..Arithmetical operation
708/520 ...Matrix array
- 3 712/208 (1 OR, 2 XR)
Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: PROCESSING ARCHITECTURES AND INSTRUCTION
PROCESSING
712/208 INSTRUCTION DECODING (E.G., BY
MICROINSTRUCTION, START ADDRESS GENERATOR, HARDWIRED)
- 3 712/228 (2 OR, 1 XR)
Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: PROCESSING ARCHITECTURES AND INSTRUCTION
PROCESSING
712/220 PROCESSING CONTROL
712/228 .Context preserving (e.g., context swapping,
checkpointing, register windowing)
- 3 712/23 (0 OR, 3 XR)
Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: PROCESSING ARCHITECTURES AND INSTRUCTION
PROCESSING
712/1 PROCESSING ARCHITECTURE
712/23 .Superscalar
- 3 712/241 (2 OR, 1 XR)
Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: PROCESSING ARCHITECTURES AND INSTRUCTION
PROCESSING
712/220 PROCESSING CONTROL
712/233 .Branching (e.g., delayed branch, loop control,
branch predict, interrupt)
712/241 ..Loop execution
- 3 712/244 (1 OR, 2 XR)
Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: PROCESSING ARCHITECTURES AND INSTRUCTION
PROCESSING
712/220 PROCESSING CONTROL

10761060_CLSTITLES.txt

712/233 .Branching (e.g., delayed branch, loop control,
branch predict, interrupt)

712/244 ..Exception processing (e.g., interrupts and
traps)

3 712/32 (2 OR, 1 XR)
Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: PROCESSING ARCHITECTURES AND INSTRUCTION
PROCESSING
712/1 PROCESSING ARCHITECTURE
712/32 .Microprocessor or multichip or multimodule
processor having sequential program control

2 345/506 (0 OR, 2 XR)
Class 345 : COMPUTER GRAPHICS PROCESSING, OPERATOR
INTERFACE PROCESSING, AND SELECTIVE VISUAL DISPLAY
SYSTEMS
345/501 COMPUTER GRAPHIC PROCESSING SYSTEM
345/502 .Plural graphics processors
345/506 ..Pipeline processors

2 382/303 (2 OR, 0 XR)
Class 382 : IMAGE ANALYSIS
382/276 IMAGE TRANSFORMATION OR PREPROCESSING
382/302 .Multilayered image transformations
382/303 ..Pipeline processing

2 708/501 (0 OR, 2 XR)
Class 708 : ELECTRICAL COMPUTERS: ARITHMETIC PROCESSING
AND CALCULATING
708/100 ELECTRICAL DIGITAL CALCULATING COMPUTER
708/200 .Particular function performed
708/490 ..Arithmetical operation
708/495 ...Floating point
708/501Multiplication followed by addition

2 711/100 (0 OR, 2 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL

2 711/169 (0 OR, 2 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/167 .Access timing
711/169 ..Memory access pipelining

2 712/200 (1 OR, 1 XR)
Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: PROCESSING ARCHITECTURES AND INSTRUCTION
PROCESSING
712/200 ARCHITECTURE BASED INSTRUCTION PROCESSING

2 712/212 (1 OR, 1 XR)
Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: PROCESSING ARCHITECTURES AND INSTRUCTION
PROCESSING
712/209 .Decoding instruction to accommodate plural
instruction interpretations (e.g., different dialects,
languages, emulation, etc.)
712/212 .Decoding by plural parallel decoders

- 2 712/216 (2 OR, 0 XR)
 Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: PROCESSING ARCHITECTURES AND INSTRUCTION
 PROCESSING
 712/216 DYNAMIC INSTRUCTION DEPENDENCY CHECKING,
 MONITORING OR CONFLICT RESOLUTION
- 2 712/218 (2 OR, 0 XR)
 Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: PROCESSING ARCHITECTURES AND INSTRUCTION
 PROCESSING
 712/216 DYNAMIC INSTRUCTION DEPENDENCY CHECKING,
 MONITORING OR CONFLICT RESOLUTION
 712/218 .commitment control or register bypass
- 2 712/22 (1 OR, 1 XR)
 Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: PROCESSING ARCHITECTURES AND INSTRUCTION
 PROCESSING
 712/1 PROCESSING ARCHITECTURE
 712/10 .Array processor
 712/16 ..Array processor operation
 712/22 ...Single instruction, multiple data (SIMD)
- 2 712/43 (0 OR, 2 XR)
 Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: PROCESSING ARCHITECTURES AND INSTRUCTION
 PROCESSING
 712/1 PROCESSING ARCHITECTURE
 712/32 .Microprocessor or multichip or multimodule
 processor having sequential program control
 712/42 ..Operation
 712/43 ...Mode switching
- 2 714/15 (0 OR, 2 XR)
 Class 714 : ERROR DETECTION/CORRECTION AND FAULT
 DETECTION/RECOVERY
 714/100 DATA PROCESSING SYSTEM ERROR OR FAULT HANDLING
 714/1 .Reliability and availability
 714/2 ..Fault recovery
 714/15 ...State recovery (i.e., process or data file)

10761060_CLS.txt
Most Frequently Occurring Classifications of Patents Returned
From A Search of 10761060 on September 08, 2005

Original Classifications

3 712/245
2 345/419
2 382/303
2 708/520
2 712/216
2 712/218
2 712/228
2 712/241
2 712/32

Cross-Reference Classifications

3 712/23
2 345/419
2 345/506
2 708/501
2 711/100
2 711/169
2 712/208
2 712/244
2 712/245
2 712/43
2 714/15

Combined Classifications

5 712/245
4 345/419
3 708/520
3 712/208
3 712/228
3 712/23
3 712/241
3 712/244
3 712/32
2 345/506
2 382/303
2 708/501
2 711/100
2 711/169
2 712/200
2 712/212
2 712/216
2 712/218
2 712/22
2 712/43
2 714/15